# CS738: Advanced Compiler Optimizations Static Single Assignment (SSA)

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## Agenda



- Constructing SSA form
- Properties and Applications

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 Developed by Ron Cytron, Jeanne Ferrante, Barry K. Rosen, Mark N. Wegman, and F. Kenneth Zadeck,

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 Static Single Assignment – A variable is assigned only once in program text

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in 1980s while at IBM.

- Static Single Assignment A variable is assigned only once in program text
  - May be assigned multiple times if program is executed

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An Intermediate Representation



- An Intermediate Representation
- Sparse representation



#### An Intermediate Representation

- Sparse representation
  - Definitions sites are directly associated with use sites

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Advantage

- An Intermediate Representation
- Sparse representation
  - Definitions sites are directly associated with use sites
- Advantage
  - Directly access points where relevant data flow information is available









#### In SSA Form

Each variable has exactly one definition



### SSA IR

#### In SSA Form

- Each variable has exactly one definition
- $\Rightarrow$  A use of a variable is reached by exactly one definition

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### SSA IR

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Control flow like traditional programs

### SSA IR

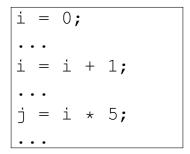
#### In SSA Form

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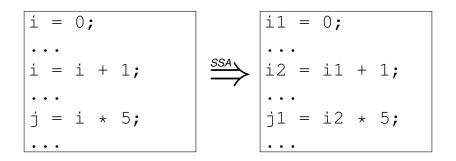
- Control flow like traditional programs
- Some *magic* is needed at *join* nodes

### Example

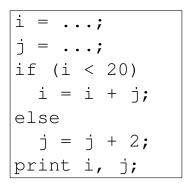


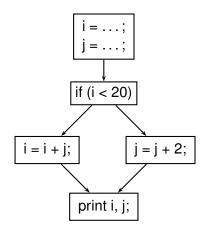
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Example

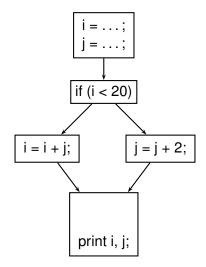


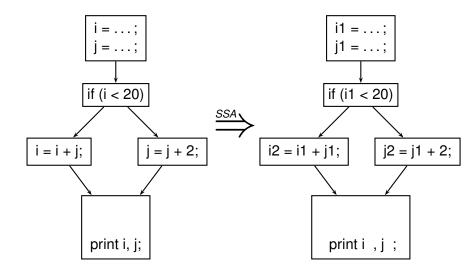
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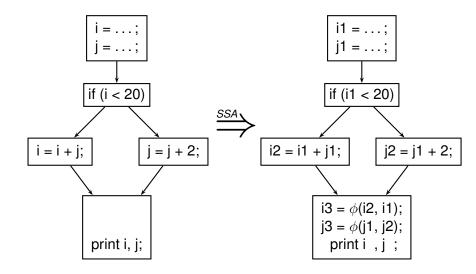


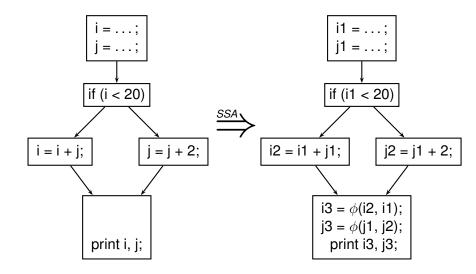
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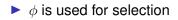


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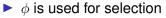




$$i = \dots; i = i + j; if (i < 20) i = i + j; else j = j + 2; j = j + 2; print i, j; j = \phi(j1, j2); print i3, j3; j = \phi(j1, j3); j = \phi(j3); j$$







One out of multiple values at join nodes



#### • $\phi$ is used for selection

One out of multiple values at join nodes

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  - Needed only if multiple definitions reach the node

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Examples?

• What does  $\phi$  operation mean in a machine code?



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- Statically equivalent to choosing one of the arguments "non-deterministicly"

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No direct translation to machine code

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Inefficient

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- $\phi$  is a conceptual entity
- Statically equivalent to choosing one of the arguments "non-deterministicly"
- No direct translation to machine code
  - typically mimicked using "copy" in predecessors
  - Inefficient
  - Practically, the inefficiency is compensated by dead code elimination and register allocation passes

Placed only at the entry of a join node

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• *n*-ary  $\phi$  function at *n*-way join node

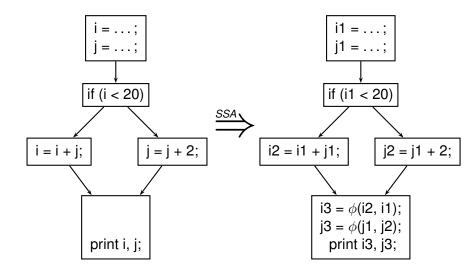
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- *n*-ary  $\phi$  function at *n*-way join node
- gets the value of *i*-th argument if control enters through *i*-th edge
  - Ordering of \u03c6 arguments according to the edge ordering is important

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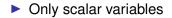
## SSA Example (revisit)



# Construction of SSA Form

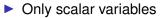
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#### Assumptions





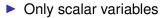
#### Assumptions



Structures, pointers, arrays could be handled

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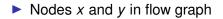
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Refer to publications



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- Nodes x and y in flow graph
- x dominates y if every path from Entry to y goes through x

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## **Computing Dominators**

Equation

$$DOM(n) = \{n\} \cup \left(\bigcap_{\substack{m \in PRED(n) \\ \forall n \in N}} DOM(m)\right),$$

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Initial Conditions:

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where *N* is the set of all nodes,  $n_{Entry}$  is the node corresponding to the *Entry* block.

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where *N* is the set of all nodes,  $n_{Entry}$  is the node corresponding to the *Entry* block.

Note that efficient methods exist for computing dominators

x is immediate dominator of y if x is the closest strict dominator of y

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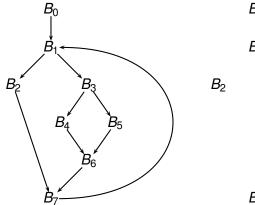
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- Dominator Tree

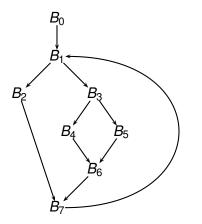
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- Dominator Tree
  - A tree showing all immediate dominator relationships

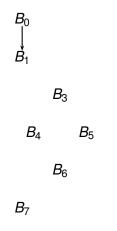
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 $B_0$  $B_1$ B<sub>3</sub>  $B_4$  $B_5$  $B_6$  $B_7$ 

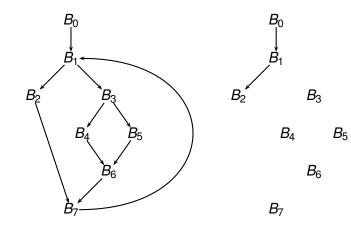
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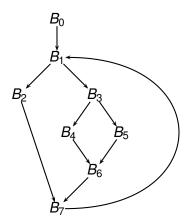


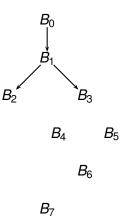


 $B_2$ 

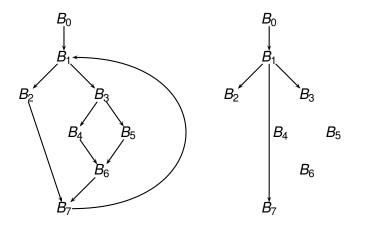
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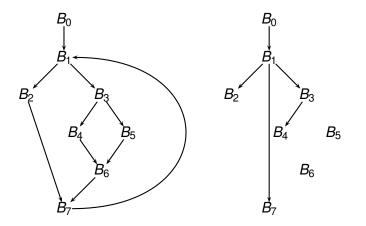




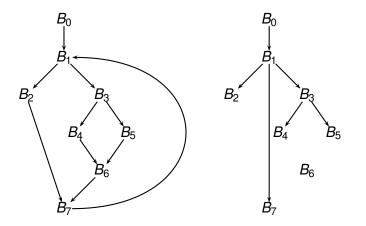


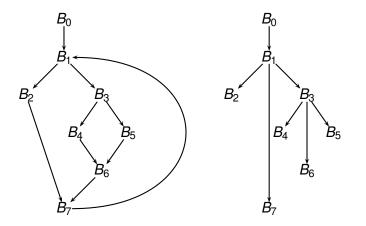
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#### Dominance Frontier: DF

Dominance Frontier of x is set of all nodes y s.t.



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#### Dominance Frontier of x is set of all nodes y s.t.

- x dominates a predecessor of y AND
- x does not strictly dominate y
- Denoted DF(x)
- Why do you think DF(x) is important for any x?
  - Think about the information originated in x.

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## Computing DF

- PARENT(x) denotes parent of node x in the dominator tree.
- CHILDERN(x) denotes children of node x in the dominator tree.
- PRED and SUCC from CFG.

$$\mathsf{DF}(x) = \mathsf{DF}_{\mathsf{local}}(x) \cup \left(\bigcup_{z \in \mathsf{CHILDERN}(x)} \mathsf{DF}_{\mathsf{up}}(z)\right)$$

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 $\mathsf{DF}_{\mathsf{local}}(x) = \{ y \in \mathsf{SUCC}(x) \mid \mathsf{idom}[y] \neq x \}$ 

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Transitive closure of Dominance frontiers on a set of nodes



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- Let S be a set of nodes

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Transitive closure of Dominance frontiers on a set of nodes

Let S be a set of nodes

 $DF(S) = \bigcup_{x \in S} DF(x)$  $DF^{1}(S) = DF(S)$  $DF^{i+1}(S) = DF(S \cup DF^{i}(S))$ 

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•  $DF^+(S)$  is the fixed point of  $DF^i$  computation.

Compute DF<sup>+</sup> set for each flow graph node

- Compute DF<sup>+</sup> set for each flow graph node
- Place trivial *p*-functions for each variable in the node

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• trivial 
$$\phi$$
-function at *n*-ary join:  $x = \phi(\overline{x, x, \dots, x})$ 

n-times

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Rename variables

- Compute DF<sup>+</sup> set for each flow graph node
- Place trivial  $\phi$ -functions for each variable in the node

• trivial  $\phi$ -function at *n*-ary join:  $x = \phi(\overbrace{x, x, \dots, x})$ 

n-times

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- Rename variables
- Why DF<sup>+</sup>? Why not only DF?

foreach variable  $\pmb{V}$  {



# foreach variable $v \in S = Entry \cup \{B_n \mid v \text{ defined in } B_n\}$

## foreach variable $v \in S = Entry \cup \{B_n \mid v \text{ defined in } B_n\}$ Compute $\mathsf{DF}^+(S)$

```
foreach variable v \in S = Entry \cup \{B_n \mid v \text{ defined in } B_n\}
Compute \mathsf{DF}^+(S)
foreach n in \mathsf{DF}^+(S) {
```

```
foreach variable v \in S = Entry \cup \{B_n \mid v \text{ defined in } B_n\}
Compute \mathsf{DF}^+(S)
foreach n in \mathsf{DF}^+(S) \in S
insert \phi-function for v at the start of B_n
```

Rename from the Entry node recursively

- Rename from the *Entry* node recursively
  - ► For each variable x, maintain a rename stack of x → x<sub>version</sub> mapping

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For node n

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For node n

For each assignment (x = ...) in n

- Rename from the Entry node recursively
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- For node n
  - For each assignment (x = ...) in *n* 
    - If non-\u03c6 assignment, rename any use of x with the Top mapping of x from the rename stack

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- Rename from the *Entry* node recursively
  - ► For each variable x, maintain a rename stack of x → x<sub>version</sub> mapping
- For node n

For each assignment (x = ...) in n

If non-\u03c6 assignment, rename any use of x with the Top mapping of x from the rename stack

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- Push the mapping  $x \mapsto x_i$  on the rename stack
- Replace lhs of the assignment by x<sub>i</sub>
- i = i + 1

For the successors of n

- Rename from the *Entry* node recursively
  - ► For each variable x, maintain a rename stack of x → x<sub>version</sub> mapping
- For node n

For each assignment (x = ...) in *n* 

If non-\u03c6 assignment, rename any use of x with the Top mapping of x from the rename stack

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- Push the mapping  $x \mapsto x_i$  on the rename stack
- Replace lhs of the assignment by x<sub>i</sub>
- i = i + 1

For the successors of n

Rename \u03c6 operands through SUCC edge index

- Rename from the *Entry* node recursively
  - ► For each variable x, maintain a rename stack of x → x<sub>version</sub> mapping
- For node n

For each assignment (x = ...) in *n* 

If non-\u03c6 assignment, rename any use of x with the Top mapping of x from the rename stack

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- Push the mapping  $x \mapsto x_i$  on the rename stack
- Replace lhs of the assignment by x<sub>i</sub>
- i = i + 1
- For the successors of n
  - Rename \u03c6 operands through SUCC edge index
- Recursively rename all child nodes in the dominator tree

- Rename from the *Entry* node recursively
  - ► For each variable x, maintain a rename stack of x → x<sub>version</sub> mapping
- For node n

For each assignment (x = ...) in *n* 

- If non-\u03c6 assignment, rename any use of x with the Top mapping of x from the rename stack
- Push the mapping  $x \mapsto x_i$  on the rename stack
- Replace lhs of the assignment by x<sub>i</sub>
- ▶ i = i + 1
- For the successors of n
  - Rename \u03c6 operands through SUCC edge index
- Recursively rename all child nodes in the dominator tree
- For each assignment (x = ...) in *n*

- Rename from the *Entry* node recursively
  - ► For each variable x, maintain a rename stack of x → x<sub>version</sub> mapping
- For node n

For each assignment (x = ...) in *n* 

- If non-\u03c6 assignment, rename any use of x with the Top mapping of x from the rename stack
- Push the mapping  $x \mapsto x_i$  on the rename stack
- Replace lhs of the assignment by x<sub>i</sub>
- ▶ i = i + 1
- For the successors of n
  - Rename \u03c6 operands through SUCC edge index
- Recursively rename all child nodes in the dominator tree
- For each assignment (x = ...) in *n* 
  - Pop  $x \mapsto \dots$  from the rename stack