# Valor: Efficient, Software-Only Region Conflict Exceptions

Swarnendu Biswas, Minjia Zhang, and Michael D. Bond Ohio State University Brandon Lucia Carnegie Mellon University

**OOPSLA 2015** 

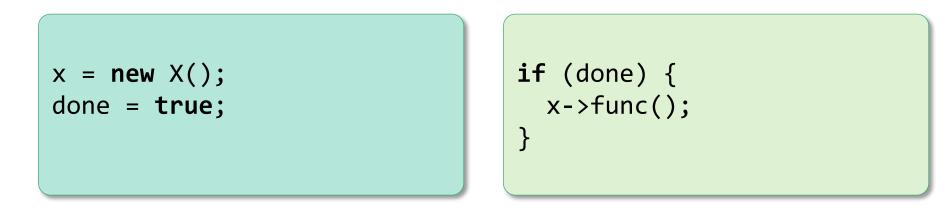


#### A Simple C++ Program

X\* x = NULL; **bool** done= **false**;

Thread T1

#### Thread T2



## A Simple C++ Program



#### Data Races

X\* x = NULL; bool done= false;

Thread T1

Thread T2



#### Data Races are Evil

No semantic guarantees

Lack of semantic guarantees make software unsafe

# Complicates language specifications

Challenging to reason about correctness for racy executions

Indicates other concurrency errors

Leads to atomicity, order or sequential consistency violations

### Catch-Fire Semantics

#### C++ treats data races as errors

X\* x = NULL; bool done= false;

Thread T1

Thread T2



### **Catch-Fire Semantics**

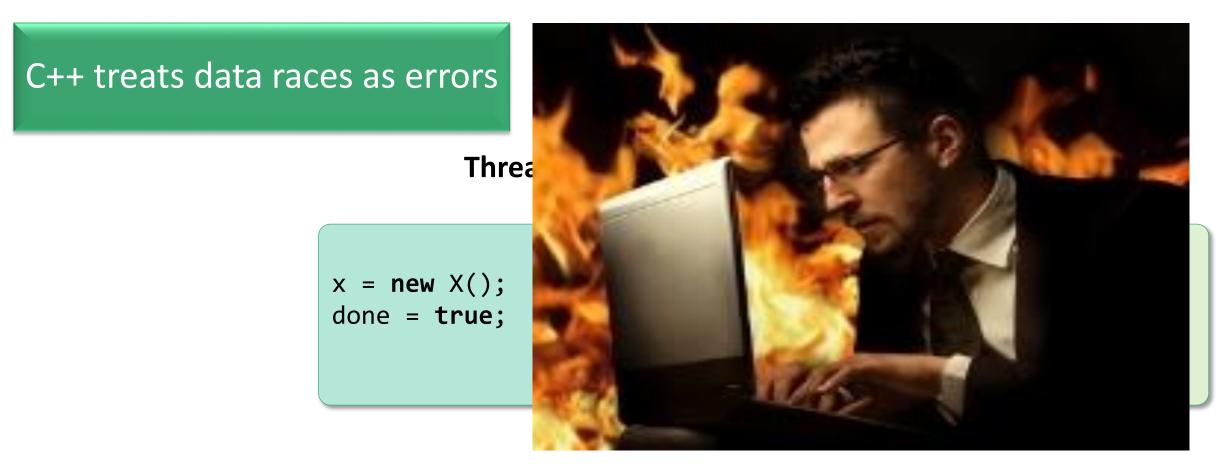
#### C++ treats data races as errors

Thread

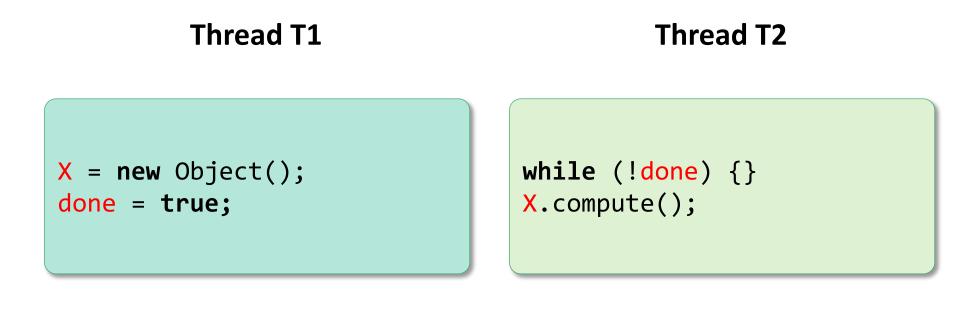
#### x = new X(); done = true;



### **Catch-Fire Semantics**



### A Java Example



### A Java Example

Java tries to assign semantics, which are unsatisfactory

#### Thread T1

#### **Thread T2**

X = new Object();
done = true;

while (!done) {}
X.compute();



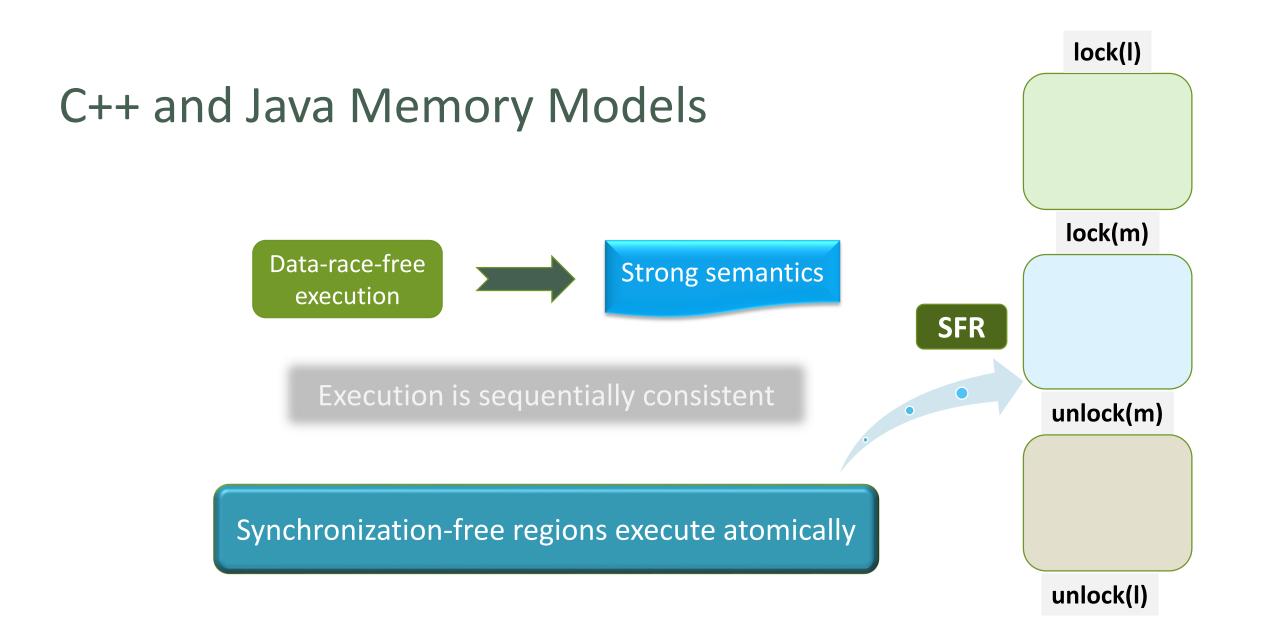


Execution is sequentially consistent



Execution is sequentially consistent

Synchronization-free regions execute atomically









### Need for Stronger Memory Models

Adve and Boehm, CACM 2010

"The inability to define reasonable semantics for programs with data races is not just a theoretical shortcoming, but a fundamental hole in the foundation of our languages and systems."

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"The inability to define reasonable semantics for programs with data races is not just a theoretical shortcoming, but a fundamental hole in the foundation of our languages and systems."

"We call upon software and hardware communities to develop languages and systems that enforce data-race-freedom, ..."

## Outline

- Programming language memory models and data races
- Data race and region conflict exceptions model
- Valor: Our contribution
- Evaluation

#### Data Race

#### Thread T1

#### Thread T2

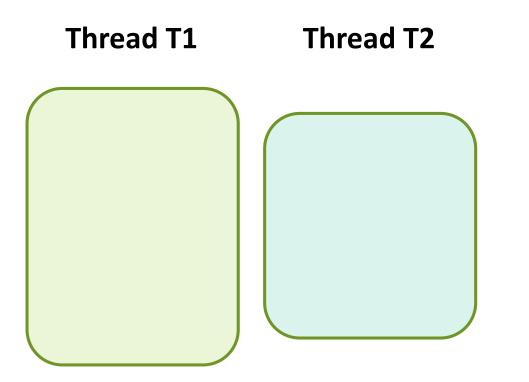
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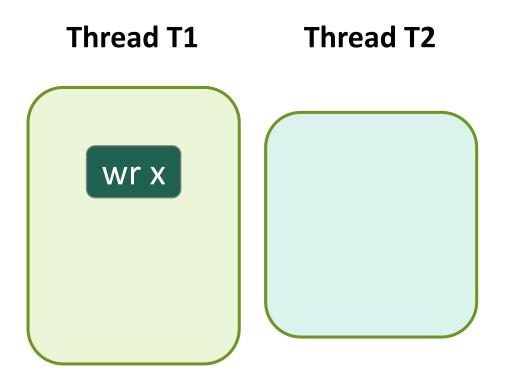
while (!done) {}
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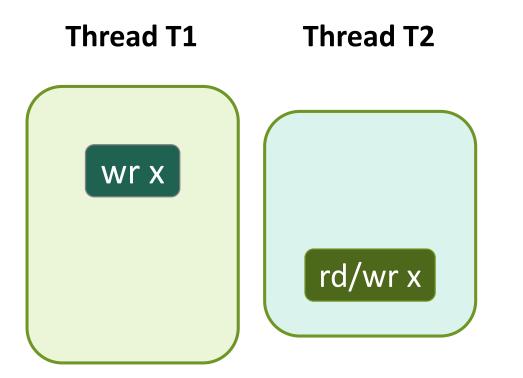
#### Data Race Exceptions

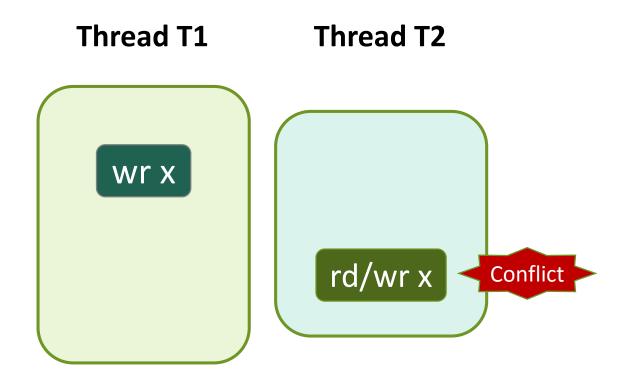


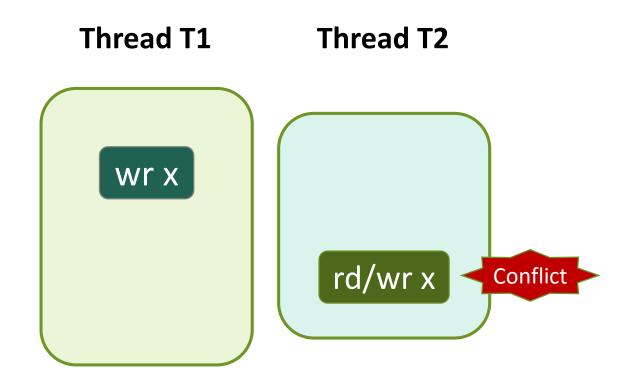
#### **REGION CONFLICT EXCEPTIONS MODEL**





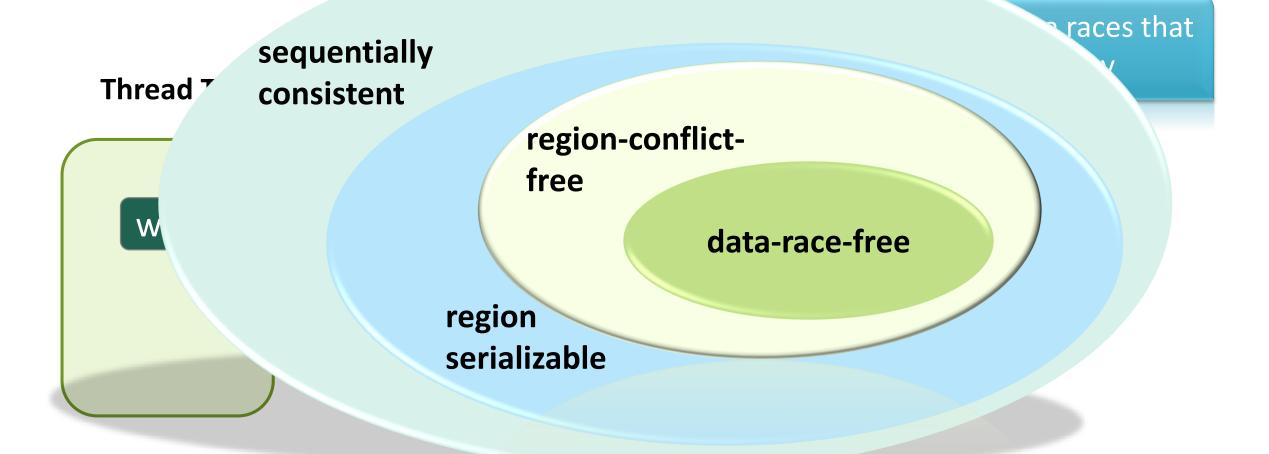






Reports a subset of true data races that violate region serializability

### **Execution Models**



### **Region Conflict Exception Model**



Develop a practical region conflict detection technique

### **Region Conflict Detection**

Hardware customizations required for good performance

Limited by resources and applicability

- Needs extensive modifications and is unscalable<sup>1</sup>
- Detects serializability violations of bounded regions<sup>2</sup>

- 1. Lucia et al. Conflict Exceptions: Simplifying Concurrent Language Semantics With Precise Hardware Exceptions for Data-Races. ISCA 2010.
- 2. Marino et al. DRFx: A Simple and Efficient Memory Model for Concurrent Programming Languages. PLDI 2010.

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#### Valor: Efficient, Software-Only Region Conflict Detector

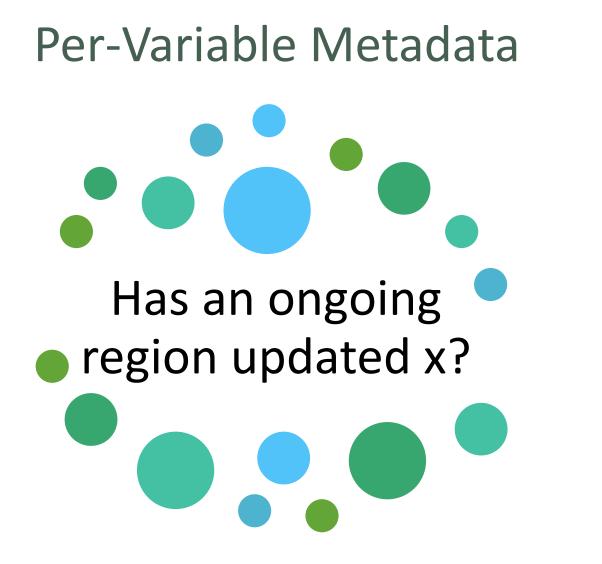
Elides tracking last readers, only tracks last writer

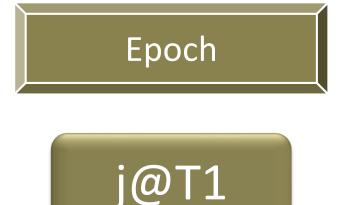
Detect read-write conflicts lazily

# Valor: Efficient, Software-Only Region Conflict Detector

- Tracking last writers
- Detecting read-write conflicts lazily
- Impact of lazy conflict detection

## Tracking Last Writer

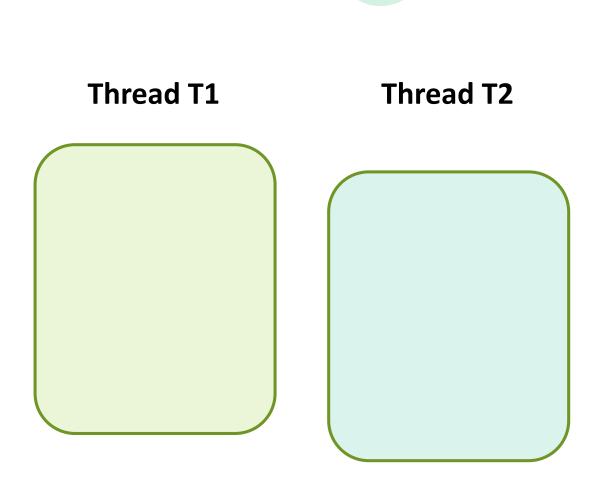




**Thread T1** 

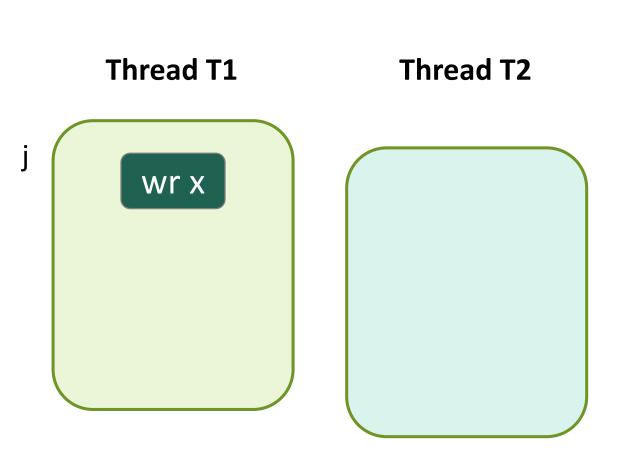


## Tracking Last Writer



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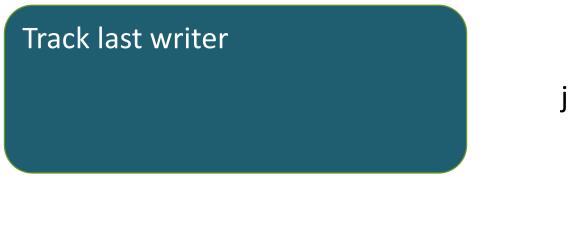
р@Т0

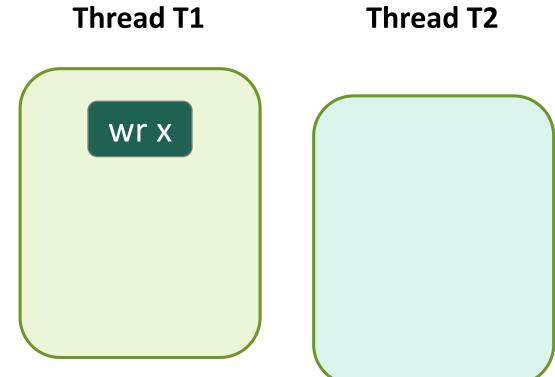


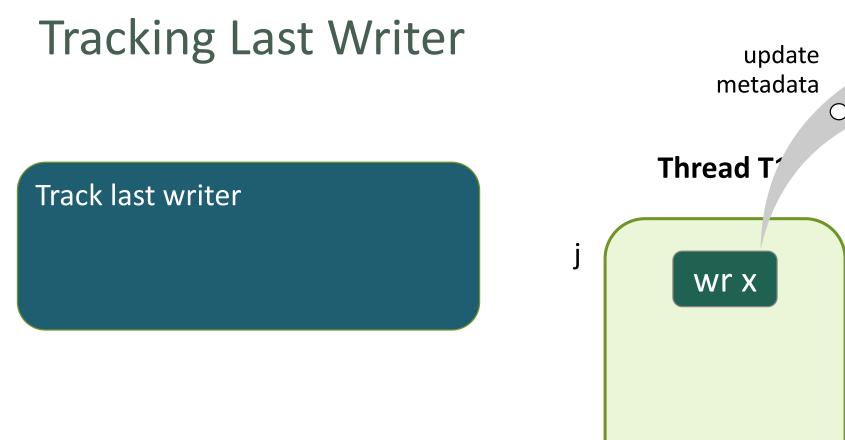
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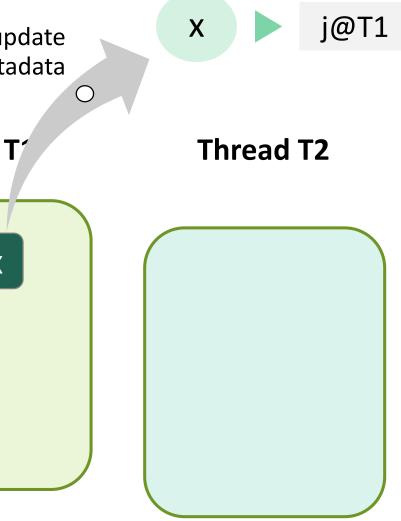
р@Т0

х р@Т0

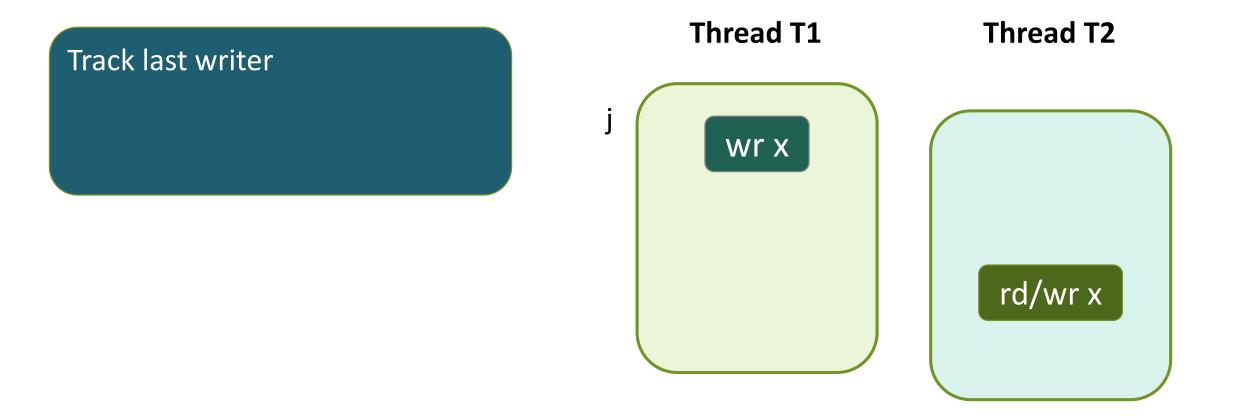






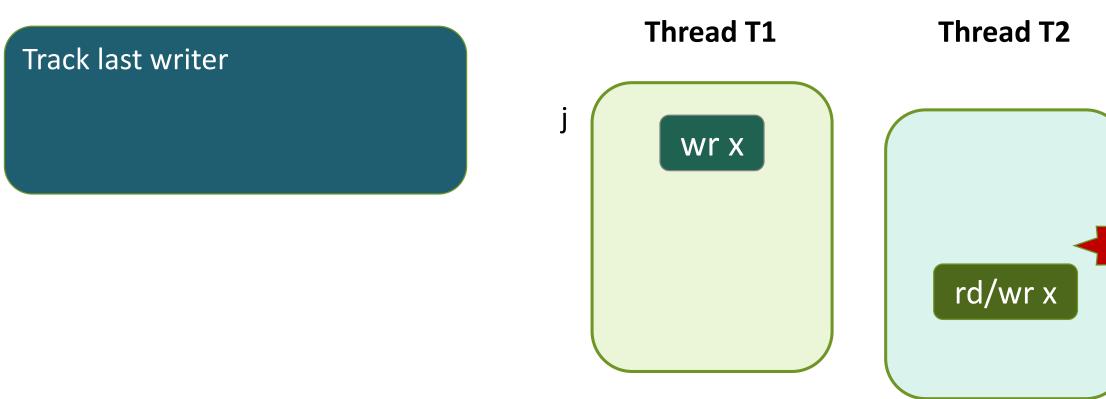


x j@T1

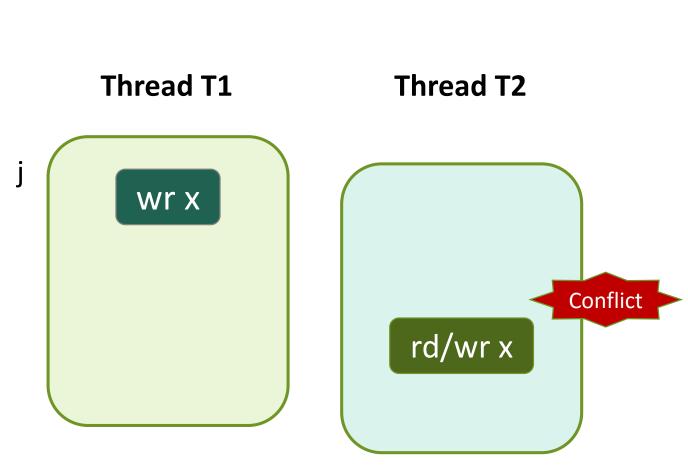


x ) j@T1

Conflict



Track last writer Allows precisely detecting write-write and write-read conflicts

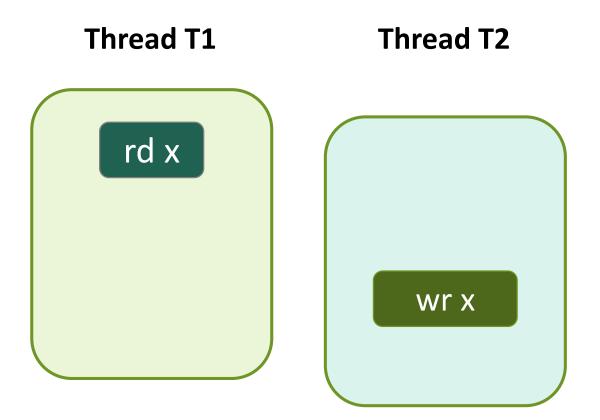


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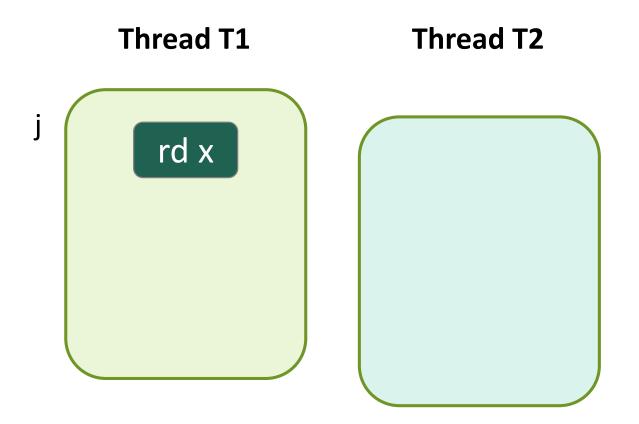
j@T1

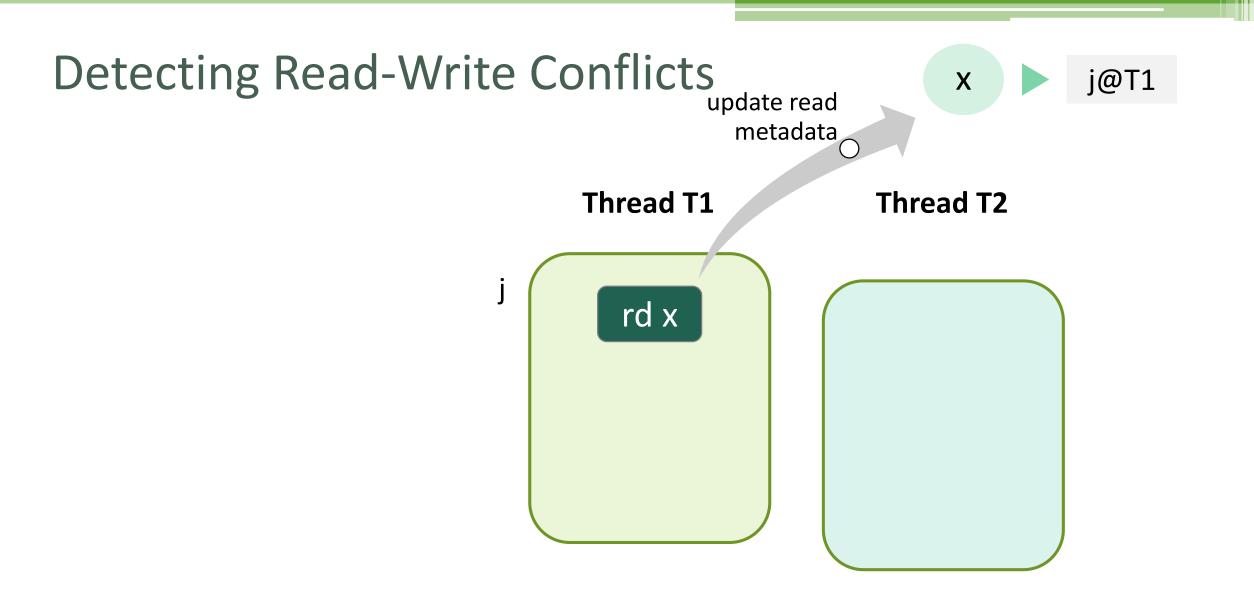
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- Tracking last writers
- Detecting read-write conflicts lazily
- Impact of lazy conflict detection

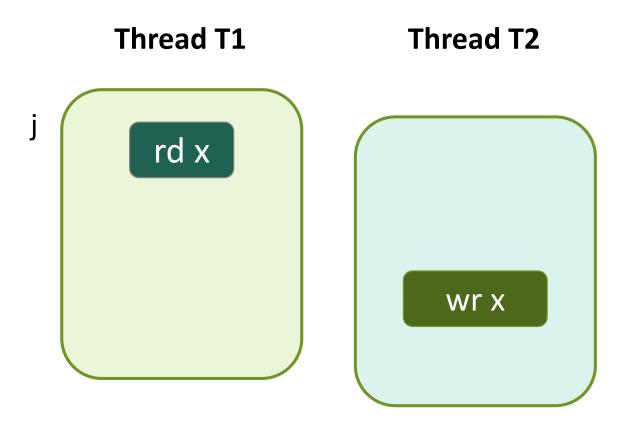




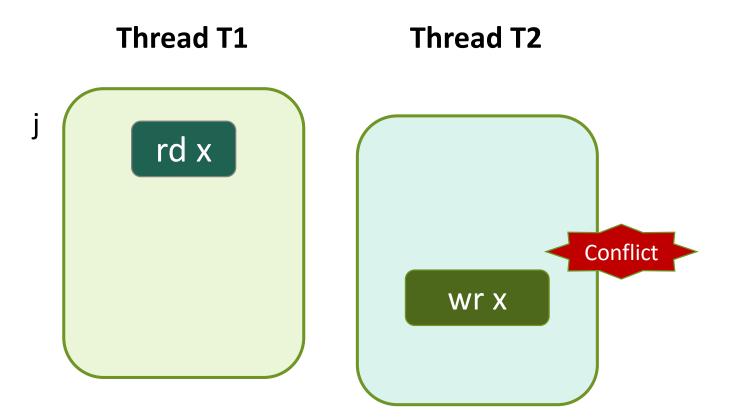


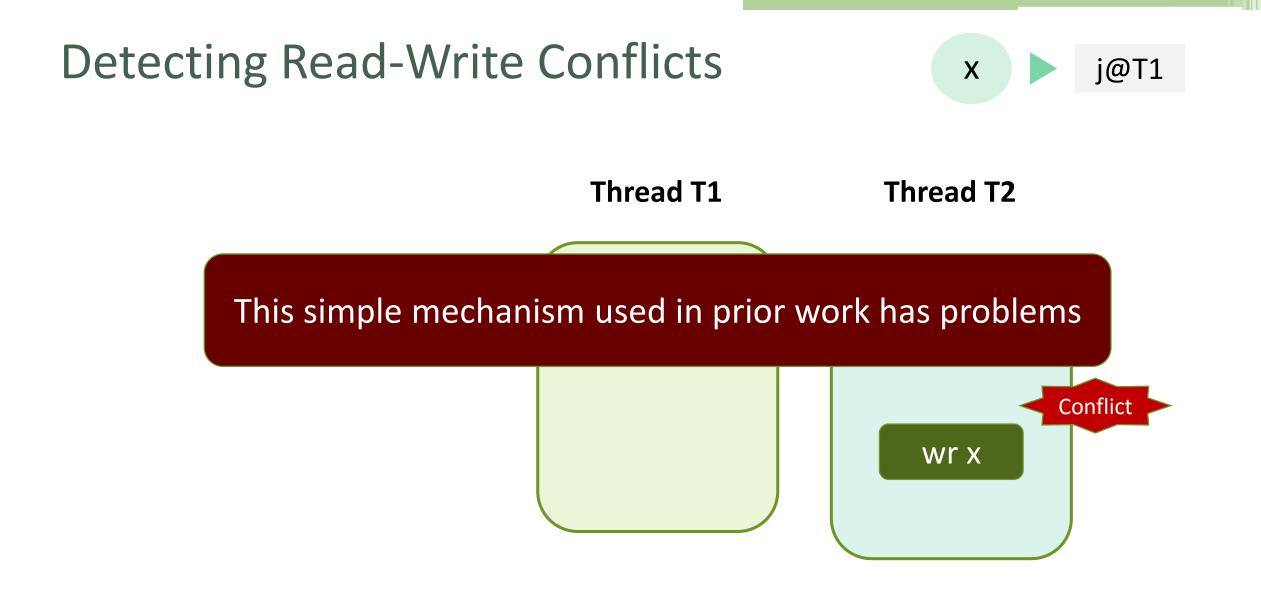




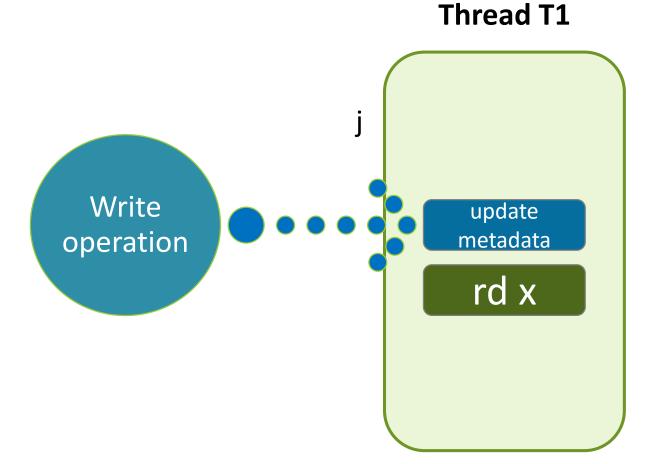






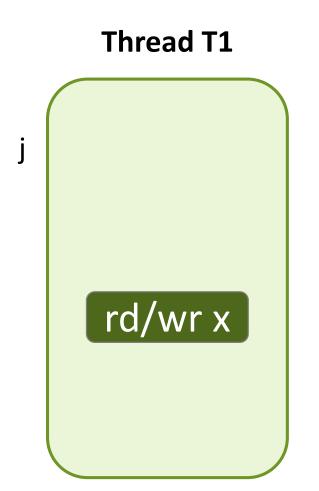


#### Remote Cache Misses Due to Tracking of Metadata

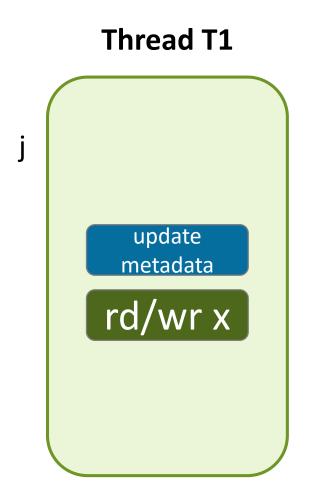


Leads to remote cache misses

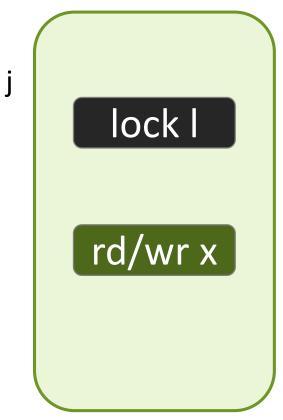




# Metadata Updates

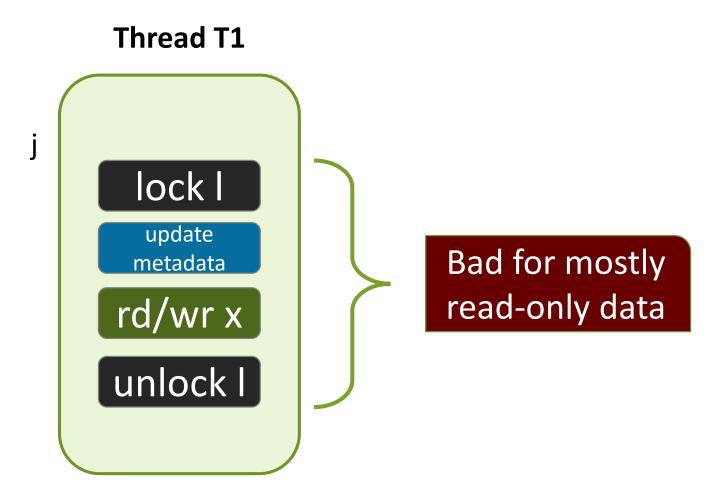


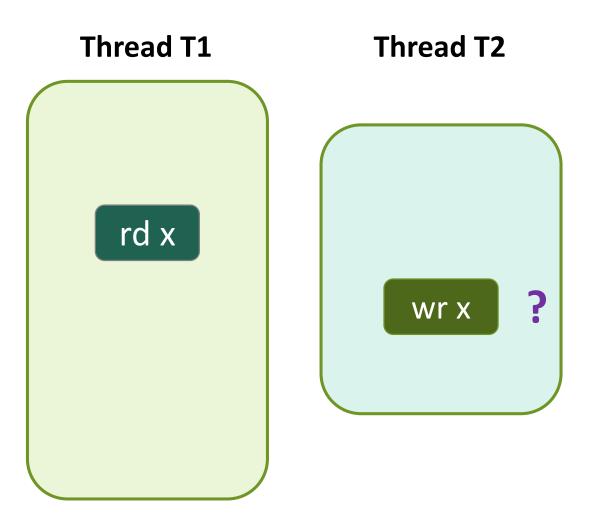
Thread T1

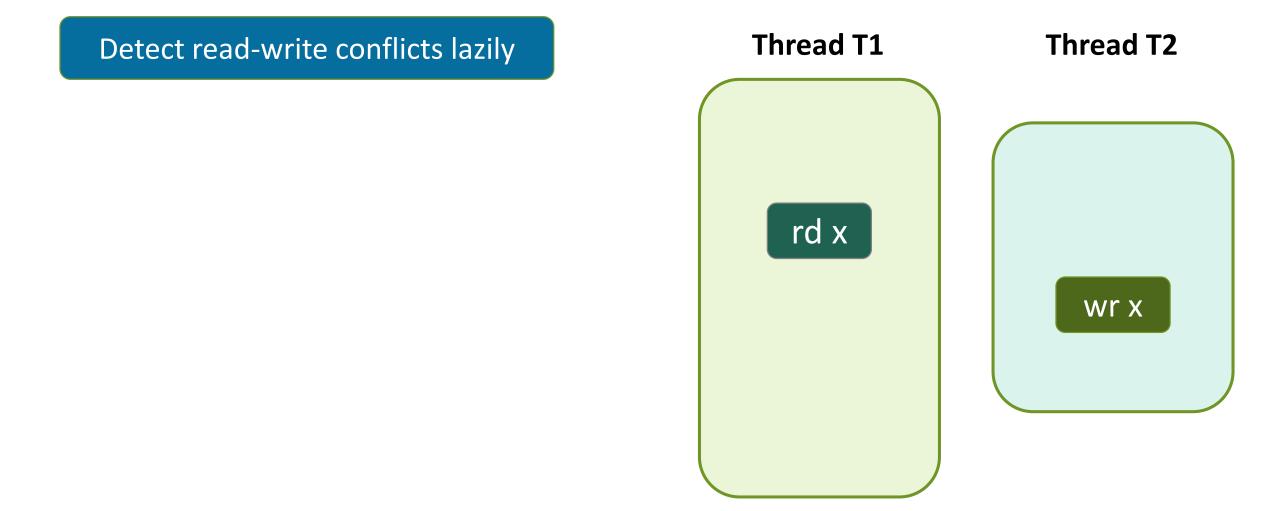


**Thread T1** lock l update metadata rd/wr x

**Thread T1** lock l update metadata rd/wr x unlock l



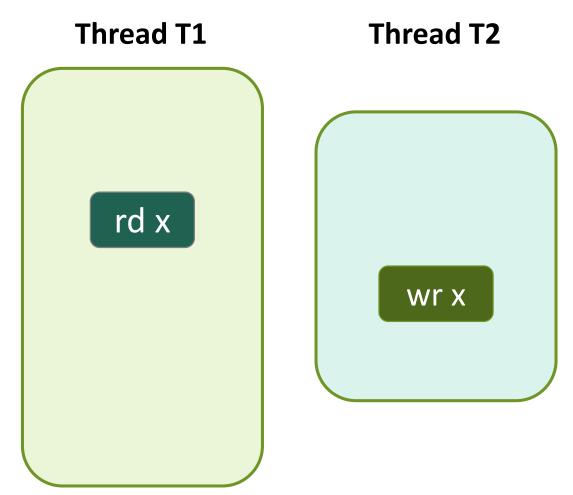


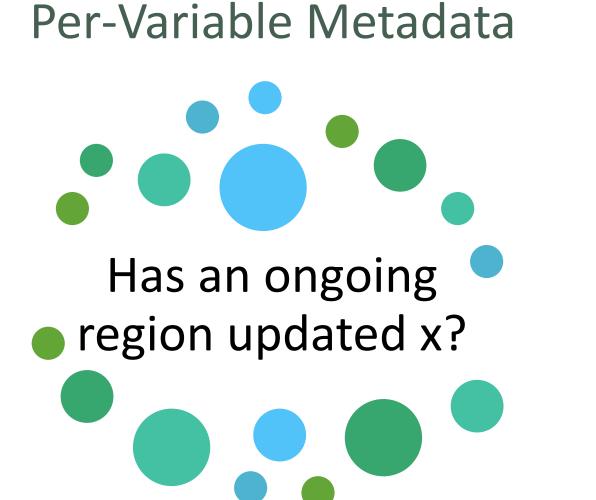


#### Detect read-write conflicts lazily

 Log read accesses in threadlocal buffers
 Validate reads at region boundaries

1. Saha et al. McRT-STM: A High Performance Software Transactional Memory System for a Multi-Core Runtime. PPoPP 2006.







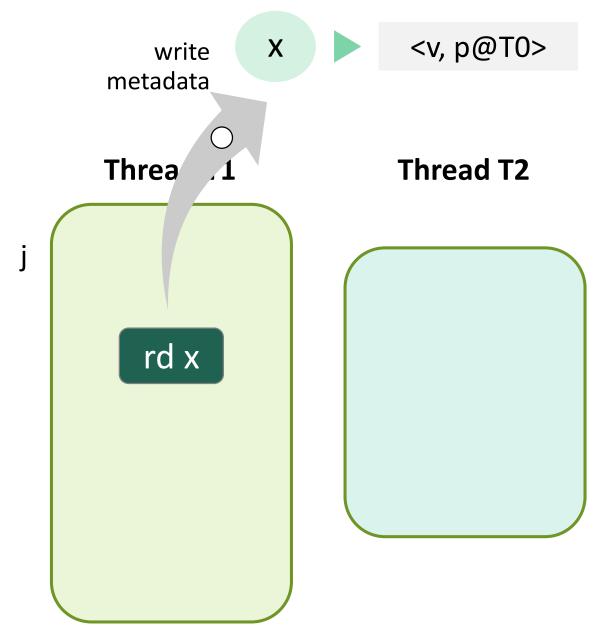
<v, j@T1>

**Thread T1** 

wrx

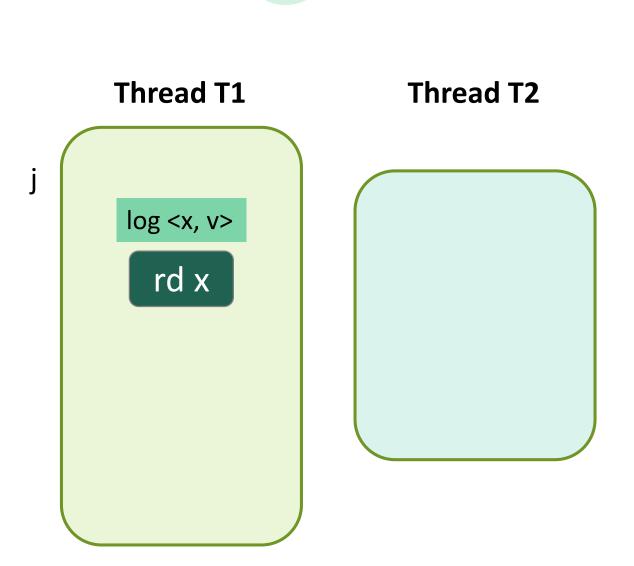
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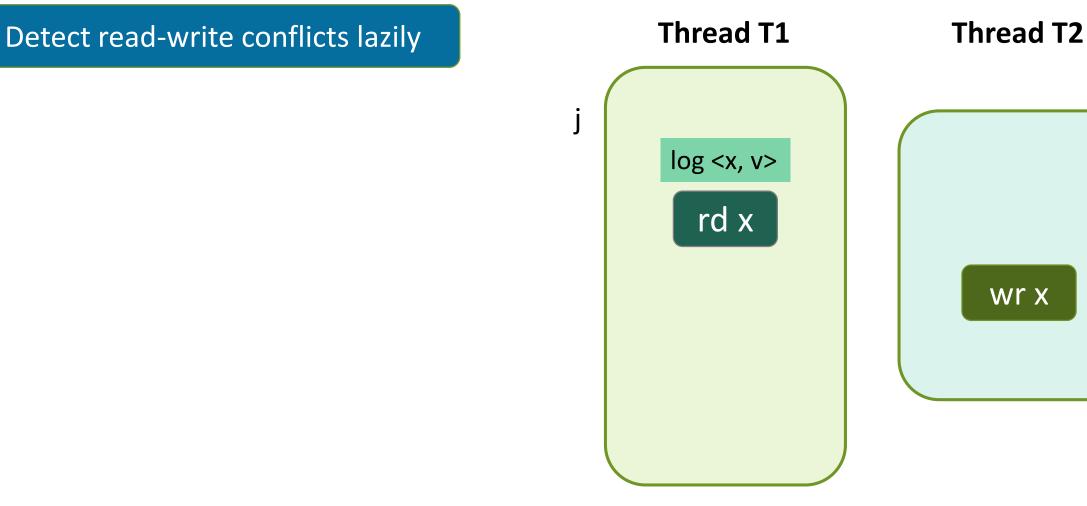
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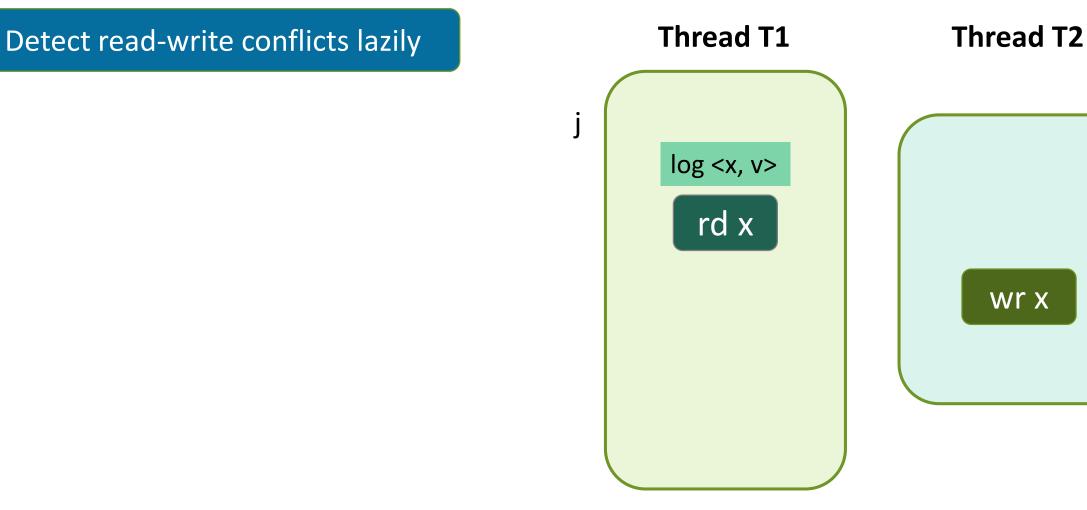
<v, p@T0>



<v, p@T0>

k

Х

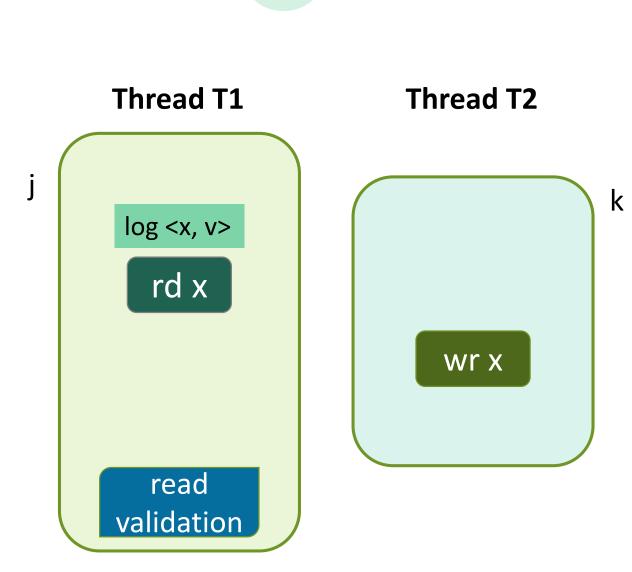


<v+1, k@T2>

k

Χ

Detect read-write conflicts lazily



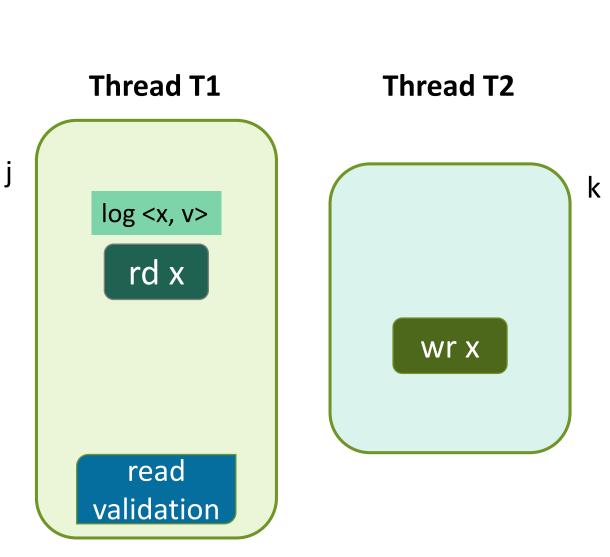
Х

<v+1, k@T2>

#### Detect read-write conflicts lazily

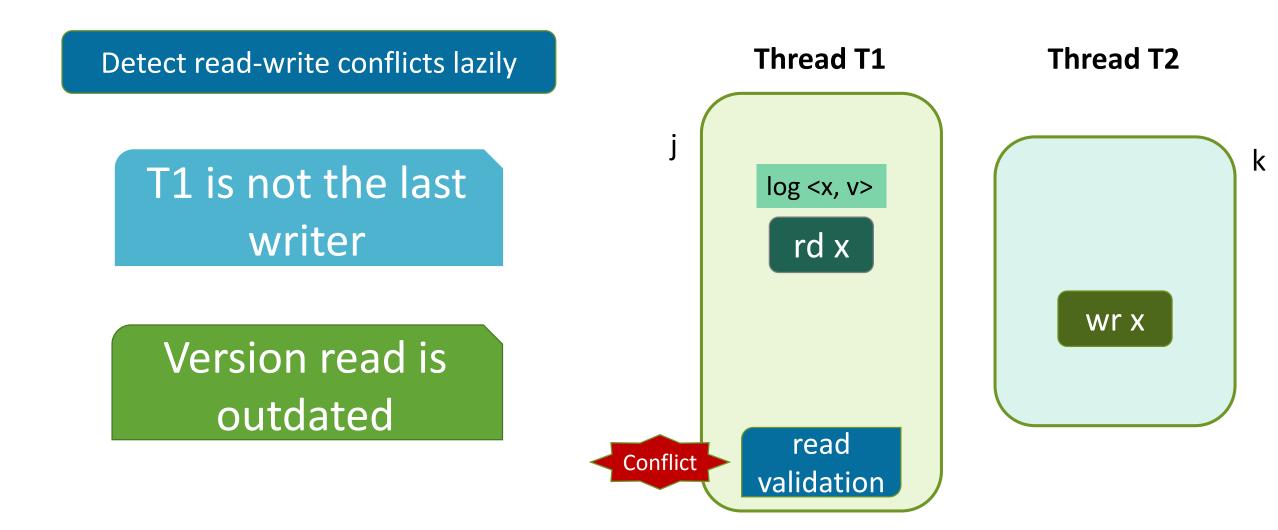
### T1 is not the last writer

Version read is outdated



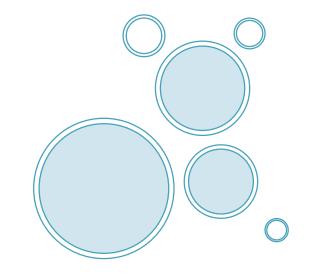
Х

<v+1, k@T2>



<v+1, k@T2>

Х



Avoids

Remote cache misses
Synchronization overhead

aynem omzarion overneda

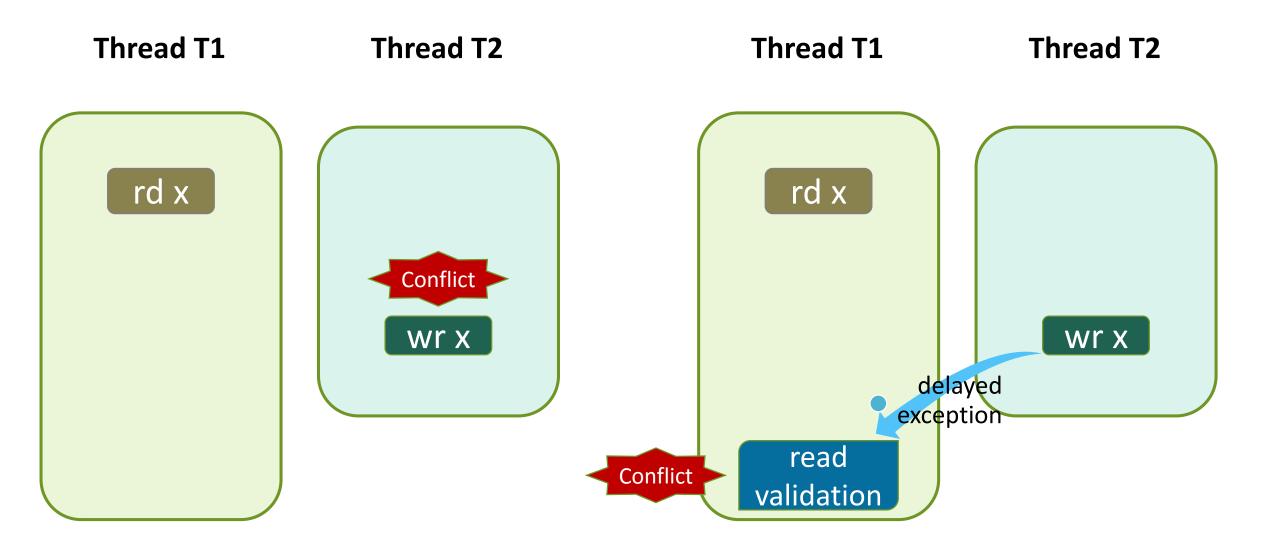
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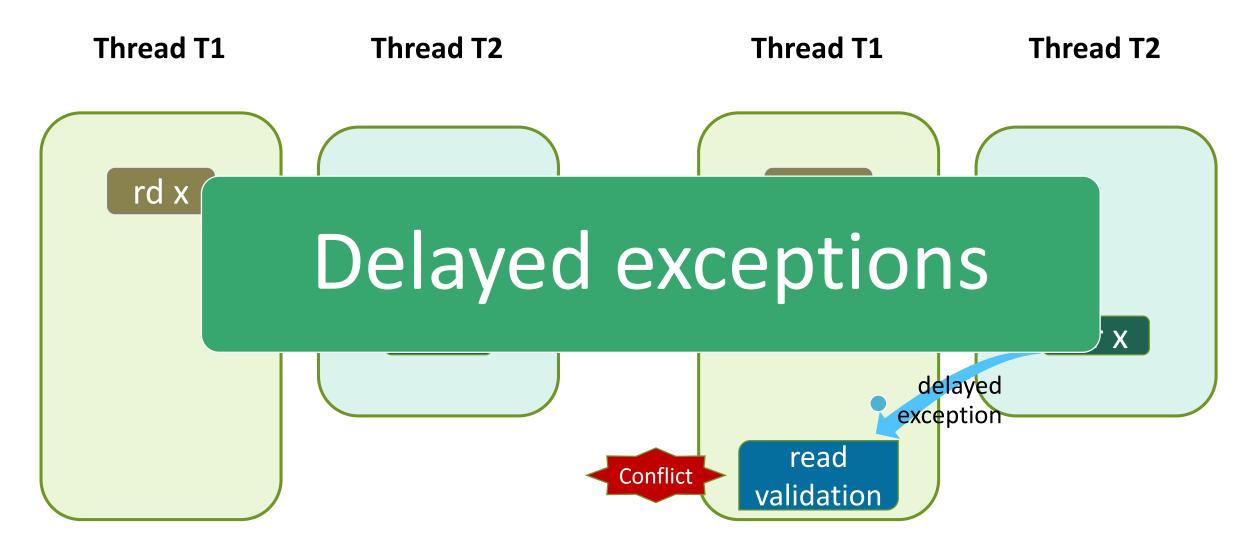
#### **Precise Conflict Detection**

Thread T1 Thread T2 rd x Conflict wrx

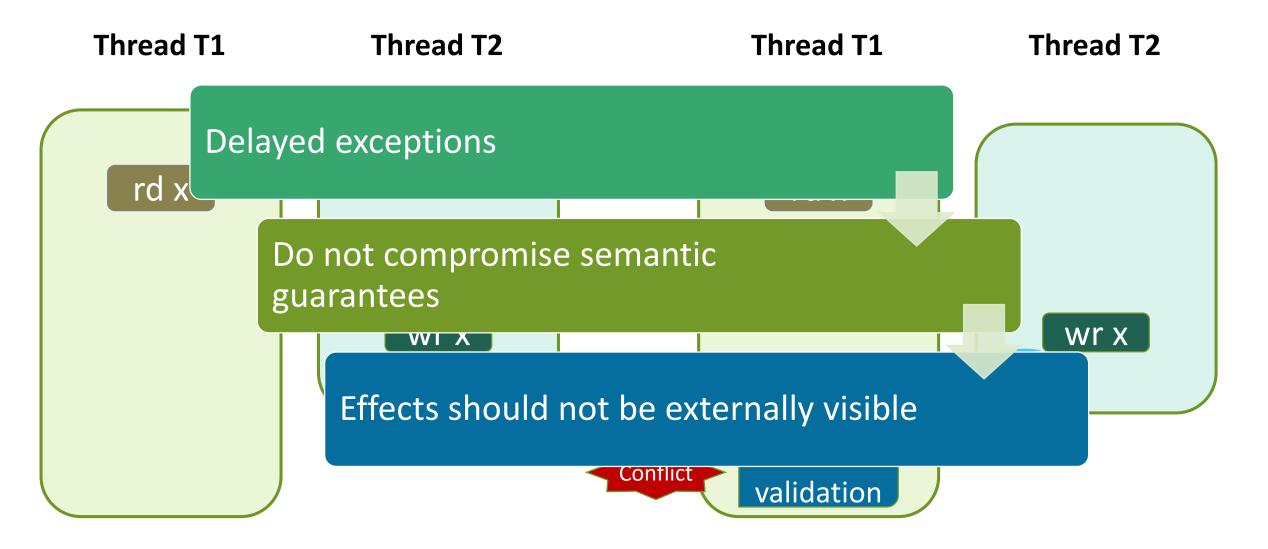
#### Precise vs Lazy Conflict Detection



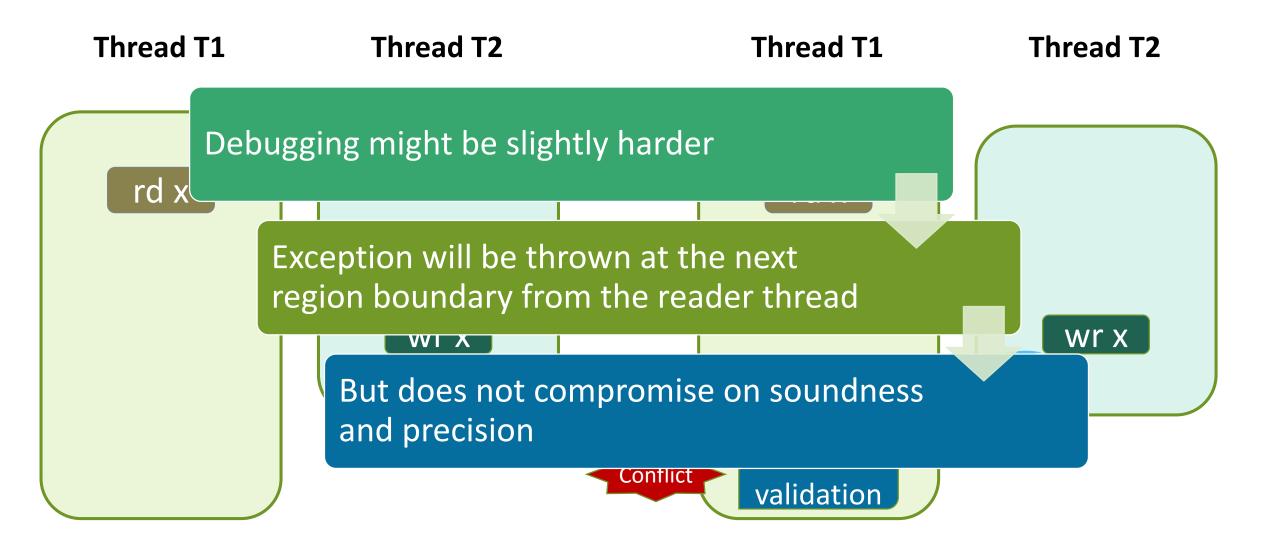
#### Precise vs Lazy Conflict Detection



### **Delayed Exceptions**



### **Delayed Exceptions**



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#### IMPLEMENTATION

#### Implementation

Developed on top of Jikes RVM 3.1.3



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Developed on top of Jikes RVM 3.1.3

Implemented FastTrack, state-of-art happens-before analysis based data race detector



1. Flanagan and Freund. FastTrack: Efficient and Precise Dynamic Data Race Detection. PLDI 2009.

#### Implementation

Developed on top of Jikes RVM 3.1.3

Implemented FastTrack, state-of-art happens-before analysis based data race detector

Shared on Jikes RVM Research Archive and ACM DL



#### EVALUATION

# **Experimental Methodology**

#### Benchmarks

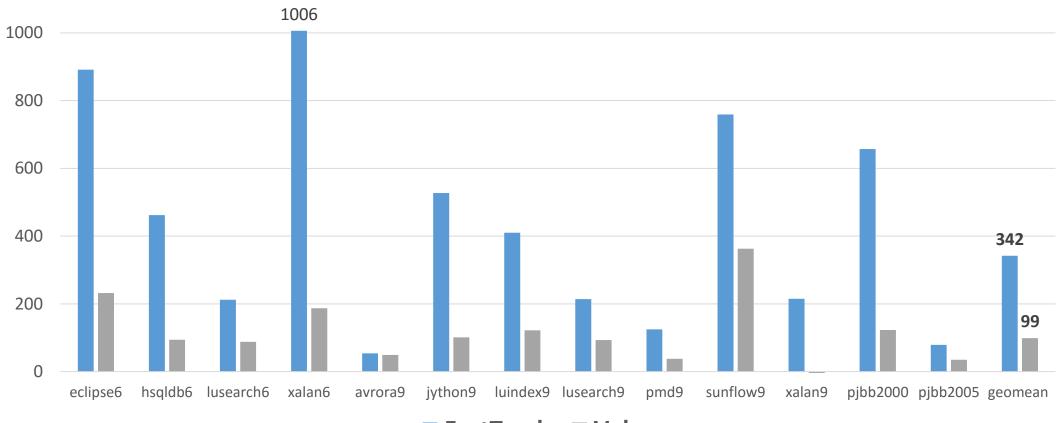
- Large workload sizes of DaCapo 2006 and 9.12-bach suite
- Fixed-workload versions of SPECjbb2000 and SPECjbb2005

#### Platform

64-core AMD Opteron 6272

#### **Performance Comparison**

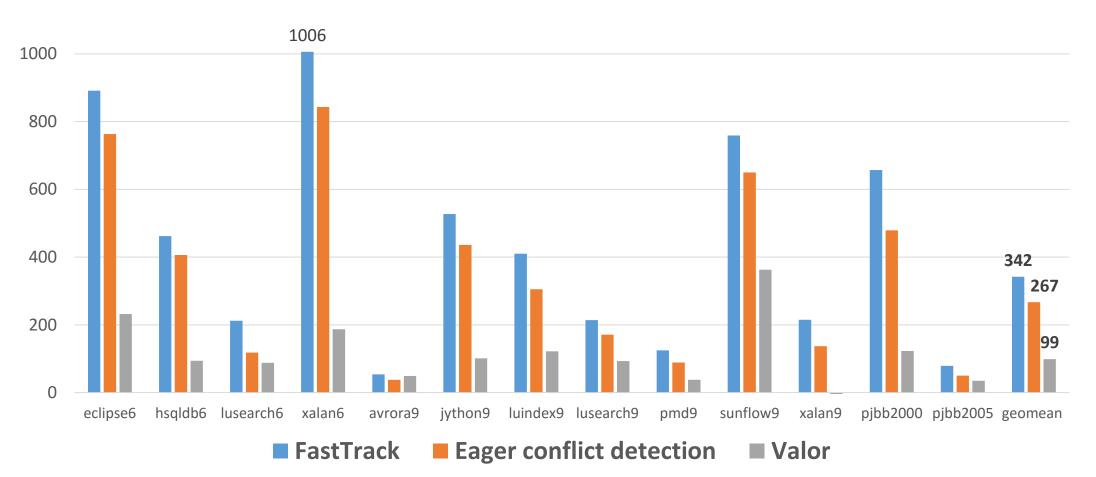
**Overheads (%) Over Unmodified Jikes RVM** 



■ FastTrack ■ Valor

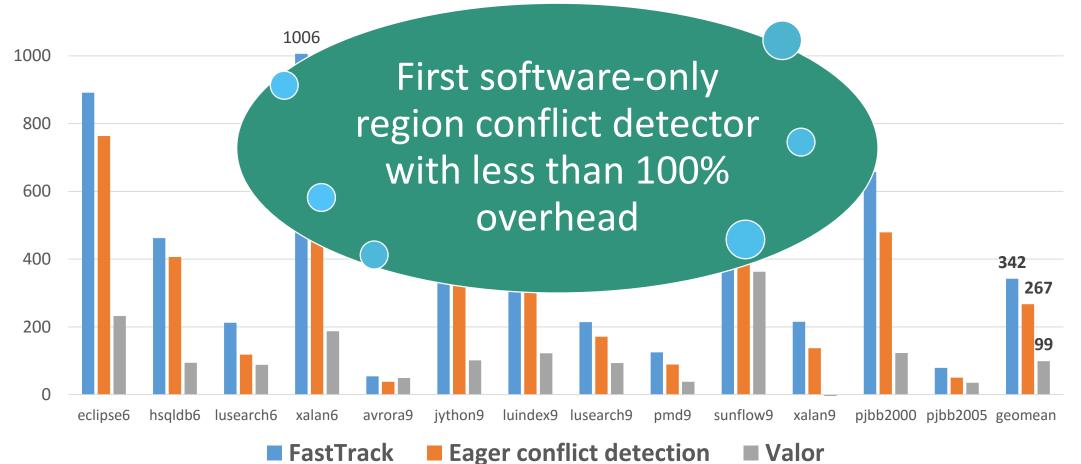
#### **Performance Comparison**

**Overheads (%) Over Unmodified Jikes RVM** 



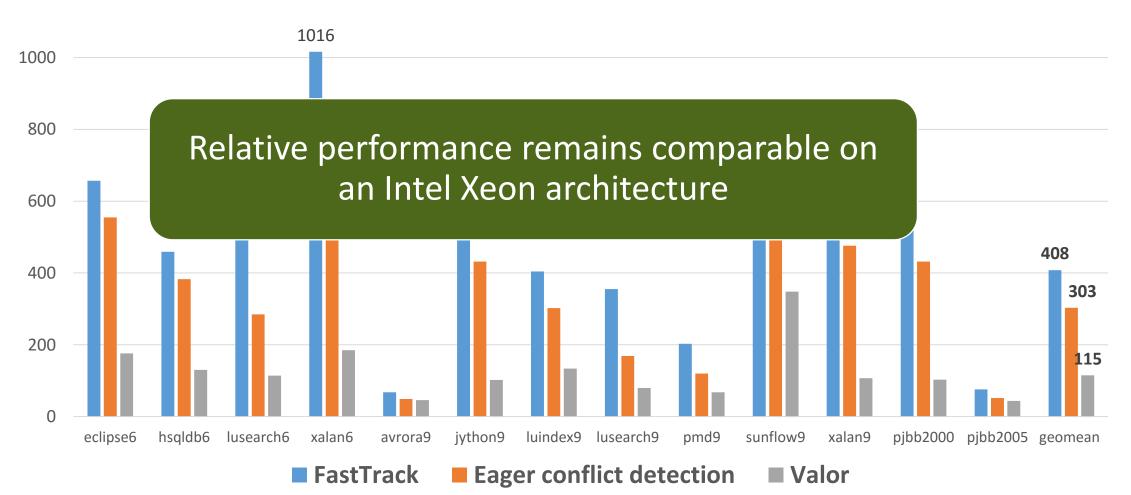
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**Overheads (%) Over Unmodified Jikes RVM** 

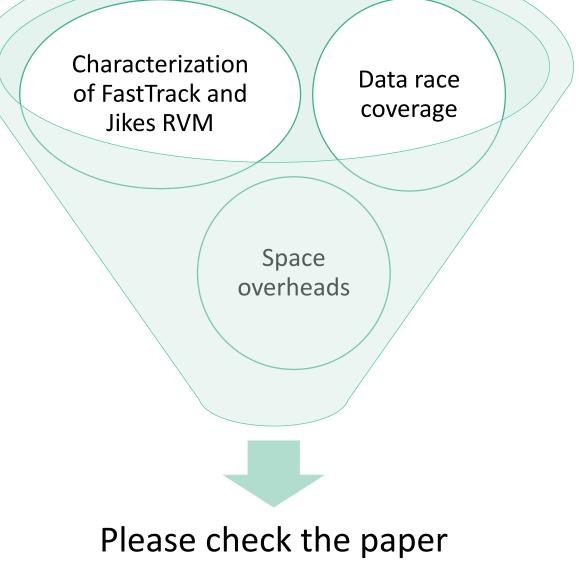


#### Performance Comparison: Intel Xeon

**Overheads (%) Over Unmodified Jikes RVM** 



#### **Additional Experiments**



#### Valor: Contributions

#### Strong execution guarantees in software

Detects all violations of region serializability



Advances state-of-art Provides strong semantics in software at less than 100% overhead

### New Opportunities with Valor

Semantic guarantees Language runtimes could integrate this

#### Debugging Can be used to detect **problematic** data races

#### **Conflict exceptions All-the-time monitoring** in certain environments

Reorder and eliminate redundant loads and stores within synchronization-

Aggressive optimizations free regions

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