

Reduction to bare minimum depth

- By the efficient $O(\ell d)$ -depth reduction we know that: to prove hardness results for a degree d polynomial f it suffices to study $O(\ell d)$ -depth.
- Now we will reduce this, further, to depth-4.

Theorem [Agrawal-Vinay '08, Koiran '12, Tavenas '15]:

Let f be a degree d polynomial computed by a size \mathcal{B} circuit. Then, for all $t \in [d]$, f has a homogeneous $\sum \prod^{O(d/t)} \sum \prod^t$ circuit of top fanin $\mathcal{B}^{O(d/t)}$ & size $\mathcal{B}^{O(t+d/t)}$.

[$\sum^k \prod^{d'} \sum \prod^t$ circuit looks like $\sum_{i=1}^k \prod_{j=1}^{d'} f_{ij}$ where k is the top fanin & the bottom fanin t bounds the degree of f_{ij} 's.]

[To optimize the size one could take $t = \sqrt{d}$, giving $k \approx \text{size} \approx \mathcal{B}^{O(\sqrt{d})}$ which is nontrivial!]

Proof: • We will use Saptharishi (2016)'s version.
 • Let C be the $O(\lg d)$ -depth circuit, of size δ , computing f . Wlog, for each internal gate g of C we have a homogeneous expr.:

$$g = \sum_{i \in [s]} g_{i1} \cdots g_{is}, \quad \dots \quad (1)$$

where for each lower gate $\deg g_{ij} \leq \frac{\deg g}{2}$.

[Recall that C can be computed in randomized poly($\delta n d$) - time.]

• In particular, the above expression (1) gives a $\sum^d \prod \sum^{d/2}$ circuit computing f .

To reach to $\sum \prod \sum^t$, we will incrementally "open" it up:

i) For each summand $g_{i1} \cdots g_{is}$, with some $\deg g_{ij} > t$, expand g_{ij} one step further (& g) using the expression (1).

ii) Repeat this process till all g_{ij} 's on the RHS have degree $\leq t$.

• Each expansion, like (i), grows the top fanin

by a multiple of δ .

We intend to show that this can happen only $O(d/t)$ times.

- In eqn.(1) if $\deg g =: d'$, then the largest degree g_{ij} in any summand has $\deg \geq d'/5$ (by homogeneity). Moreover, the second largest degree is $\geq \frac{1}{4} \cdot \left(\frac{d'}{2}\right)$, as $\deg g_{ij} \leq d'/2$.
 \Rightarrow in each summand there are two factors of degree $\geq d'/8$.

\Rightarrow Whenever we expand by eqn.(1), a factor of $\deg \geq t$, we introduce at least one more factor of $\deg \geq t/8$ (in each new summand).

- Note that, by homogeneity, there can be $\leq 8d/t$ factors (in a summand) of $\deg \geq t/8$.

\Rightarrow the number of iterations is $\leq 8d/t$.

\Rightarrow The eventual #summands = $\geq^{O(d/t)}$.

- Note that the factors in a summand can have at most n^t many monomials.
 \Rightarrow eventually, C converts to a $\sum \prod \sum \prod^t$ circuit with top fanin $\delta^{O(d/t)}$ & size $\delta^{O(t+d/t)}$. □

Corollary: An n -var. d -deg polynomial f requires homogeneous $\sum \prod^{O(d/t)} \sum \prod^t$ circuits of top fanin $n^{w(d/t)}$
 $\Rightarrow f$ requires arithmetic circuits of size $n^{w(1)}$.

Proof:

- We just proved its contrapositive!
□
- Could we reduce this $\sum \prod \sum \prod^t$ circuit to a $\sum \prod \sum$ one (nontrivially)?
- YES, over zero characteristic fields.

Depth-3 Chasm

Theorem [Gupta, Kamath, Kayal, Saptharishi; 2013]:

Let f be a deg- d polynomial computed by a size- s circuit over \mathbb{F} ($\text{char } \mathbb{F} = 0$). Then, there is a $\Sigma\Pi\Sigma^{\sqrt{d}}$ circuit of size $s^{O(\sqrt{d})}$ computing f .

[$\Sigma\Pi\Sigma^m$ circuit looks like $\sum_i \prod_j l_{ij}$, where k is the top fanin & each $l_{ij} \in \mathbb{F}^{[k] \times [d_i]}$, is a linear polynomial in some m variables.]

[In the above thm. we get inhomogeneous $\Sigma\Pi\Sigma$ where both k & d_i could be $s^{\sqrt{d}}$.]

Corollary: Over \mathbb{Q} , \det_n has a $n^{O(\sqrt{n})}$ -size $\Sigma\Pi\Sigma^{\sqrt{n}}$ circuit.

Conjecture: 1) \det_n requires $n^{\Omega(\sqrt{n})}$ -size $\Sigma\Pi\Sigma$.
* 2) perm_n requires $n^{\Omega(n)}$ -size $\Sigma\Pi\Sigma^{\sqrt{n}}$.

[Weaker: optimality of Ryser's formula
 $\Rightarrow \text{VP} \neq \text{VNP}$.]

- The proof requires a host of ideas.
One common feature is to use powers basis, instead of the standard basis of monomials, to express polynomials.

- Outline: Circuit $\xrightarrow{\text{Step 0}} \sum \prod \sum \prod \xrightarrow{\text{Step 1}}$
 $\sum \wedge \sum \wedge \sum$ circuits $\xrightarrow{\text{Step 2}} \sum \prod \sum (\text{over } \mathbb{C})$
 $\xrightarrow{\text{Step 3}} \sum \prod \sum (\text{over } \mathbb{Q})$.

Step 0: • Let f have a size- d_0 circuit $C_0(x_1, \dots, x_n)$.

• By depth-4 reduction we get a size $d_1 = d_0^{O(\sqrt{d})}$ homogeneous $\sum \prod^{O(\sqrt{d})} \sum \prod^{\sqrt{d}}$ circuit C_1 .

Step 1: • First, we show a general way to "change basis" that converts " \prod " to " $\sum \wedge$ ":

Lemma (Fischer's trick '94): Over $\text{ch}(\mathbb{F}) \geq r$ or zero, any expression $g = \sum_{i \in [k]} \prod_{j \in [r]} g_{ij}$, $\deg g_{ij} \leq s$, can