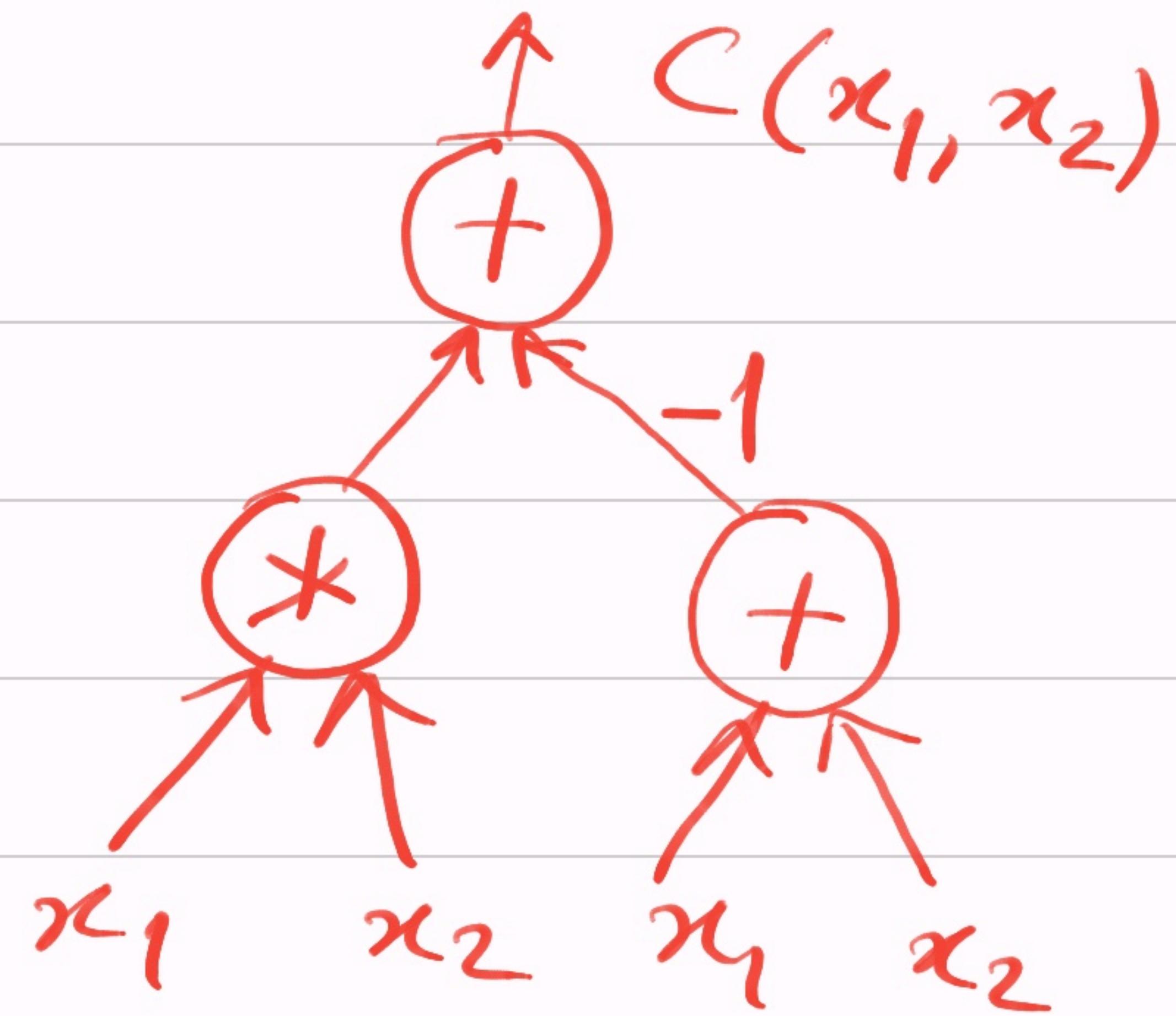


- Arithmetic circuit: A rooted tree with inputs as leaves, output as root, $+$ / $*$ as nodes, & constants on edges.



- Size = #edges + #nodes + bit-size(constants)

▷ Circuit can capture very large polynomials, in small-size!

$$\text{e.g. } (x_1 + \dots + x_n)^n \rightarrow \approx 2^n \text{ monomials}$$

▷ PIT is nontrivial. We want $\text{poly}(\text{size})$ -time.

Theorem: PIT \in BPP.

Pf: • Idea: evaluate circuit at random point.
• Let $C(\bar{x})$ be the given circuit, over F , of size δ .

▷ $\deg C(\bar{x}) \leq \delta^{\delta}$. [Each *-gate could grow the degree δ -times.]

• Pick a subset $S \subset F$ s.t. $|S| > 2 \cdot \delta^{\delta}$.

[If F is small then go to a field extension.]

• Algorithm samples from S :

1) Pick random point $(a_1, \dots, a_n) \in S^n$ $\stackrel{:=}{=} \bar{a}$

2) If $C(\bar{a}) = 0$ then OUTPUT zero else OUTPUT nonzero.

• Correctness:

▷ $C(\bar{x}) = 0 \Rightarrow \text{Prob}[\text{correct output}] = 1.$

▷ $C(\bar{x}) \neq 0 \Rightarrow \text{Prob}[\text{correct output}]$

[P.I. lemma] $\geq 1 - \frac{\delta^d}{|S|^d} = \frac{1}{2}.$

▷ Error prob. can be reduced by repeating. □

P.I. lemma (DeMillo-Lipton'78, Zippel'79, Schwartz'80) :

Let $P \in \mathbb{F}[\bar{x}]$ be a polynomial of degree $d \geq 0$.

Then, $\Pr_{\bar{a} \in S^n} [P(\bar{a}) = 0] \leq d/|S|.$

Proof: • For $n=1$, it follows from the fact that $P(x_1)$ has $\leq d$ roots in \mathbb{F} .

- Let's induct on n :
- Assume it to be true for $(n-1)$ -variables.
- Write $P = \sum_{0 \leq i \leq d} x_n^i \cdot \underline{P_i(x_1, \dots, x_{n-1})}$.
- As $P \neq 0$, let i_0 be the largest i s.t. $P_{i_0} \neq 0$.

$$\begin{aligned}\Rightarrow \Pr[P(\bar{a})=0] &= \Pr[P_{i_0}(\bar{a})=0] \cdot \Pr[P(\bar{a})=0 \mid P_{i_0}(\bar{a})=0] \\ &\quad + \Pr[P_{i_0}(\bar{a}) \neq 0] \cdot \Pr[P(\bar{a})=0 \mid P_{i_0}(\bar{a}) \neq 0] \\ &\leq \Pr[P_{i_0}(\bar{a})=0] + \Pr[P(\bar{a})=0 \mid P_{i_0}(\bar{a}) \neq 0] \leq \frac{d-i_0}{|S|} + \frac{i_0}{|S|} \\ &\leq d/|S|. \quad \square \text{ induction} \xrightarrow{|S|} |S|\end{aligned}$$

-Exercise: How do you construct extension of IF?
[do it in $\text{poly}(s)$ -time?]

The Circuit Model

- An arithmetic circuit (over \mathbb{F}) has $+$, $*$ gates & field elements.
 \Rightarrow computes polynomials.
- A boolean circuit has \wedge , \vee , \sim gates & constants $= \{0, 1\}$ or $\{\text{False}, \text{True}\}$.
 \Rightarrow computes boolean formula.
- We can use these as a model of computation instead of TMs.

- Defn: • A problem $L \subseteq \{0,1\}^*$ is said to be solved by a boolean circuit family $\{C_n(x) | n \geq 1\}$ if $\forall n, \forall x \in \{0,1\}^n, C_n(x) = 1$ iff $x \in L$.

• Computational resources are: size(C_n), depth(C_n) & fanin/fanout (C_n).

- You can prove the following simple facts:

Proposition: 1) Any TM can be turned into a boolean circuit family. [vice versa?]

2) Boolean circuits are inspired from "electronics"
& capture parallel computation:

$\text{size}(C) \rightsquigarrow$ space of the parallel algorithm.
 $\text{depth}(C) \rightsquigarrow$ time " " " "

3) Two n -bit integers can be added by a
 $\text{poly}(n)$ -size & constant-depth boolean circuit.

[Exercise: What about multiplying two n -bit
integers?]

Circuit Complexity Classes

- Analogous to $\text{Dtime}(T(n))$ we have,

Size($s(n)$) := $\{ L \subseteq \{0,1\}^* \mid \exists \text{ O}(s(n))\text{-size boolean circuits } \{C_n\} \text{ solving } L \}$.

- P/poly := $\bigcup_{c \geq 0} \text{Size}(n^c)$.

- Analogously, for arithmetic circuits :

Ar-size($s(n)$) := $\{ \{f_n\}_{n \geq 1} \mid \exists \text{ O}(s(n))\text{-size arithmetic circuits } \{C_n\}_n \text{ s.t. } C_n = f_n \}$

- Ar-P/poly := $\bigcup_{c \geq 0} \text{Ar-Size}(n^c)$.

$\triangleright \{ \begin{array}{l} P/\text{poly} \text{ has boolean functions } f_n : \{0,1\}^* \rightarrow \{0,1\} \\ \text{Ar-P}/\text{poly} \text{ has polynomial } f_n : F^n \rightarrow F \end{array} \}$

$\triangleright P \not\subseteq P/\text{poly}$

- But $\text{Ar-P}/\text{poly}$ is incomparable with P .